Global Communication Semiconductors, LLC



Corporate & Foundry 23155 Kashiwa Court, Torrance, CA. 90505 Telephone: 310-530-7274 Fax: 310-517-8200 Website: www.gcsincorp.com

Job Title:	Device/Process Integration Engineer
Opened:	2/23/18
Job Req.	#0643
Department:	RF and Power Technology
Status:	Full-Time/ Exempt
Shift:	Day

Job Description

- Responsible for GaN HEMT process development and yield improvement efforts, including 1.0 um, 0.5 um and 0.25, 0.15 um processes for RF and power electronics applications.
- Responsible for device characterization and process qualifications
- Support trouble-shooting and yield improvement in pHEMT, HFET and MESFET production processes.

Job Requirements

- PhD in Electrical Engineering degree, Physics or Material Science
- 3-5 years of industry experience in a semiconductor wafer fab environment
- Familiarity with compound semiconductor devices, processing and handling a plus