



# **Global Communication Semiconductors, LLC**

Corporate & Foundry

23155 Kashiwa Court, Torrance, CA. 90505

Telephone: 310-530-7274 Fax: 310-517-8200

Website: [www.gcsincorp.com](http://www.gcsincorp.com)

Job Title: **Sr. Device / Integration Engineer**

Job Req: #0566

Department: Technology

Status: Full-Time/Exempt

## **Job Description**

Lead FBAR and Wafer-Level-Package process development and yield improvement efforts and support customer transferred processes.

- Responsible for process characterization, yield enhancement and process qualifications.
- Responsible for specific module processes and equipment qualification related to FBAR processes.

## **Job Requirements**

- PhD or MS in Electrical Engineering, Physics or Materials Science.
- More than 10 years of industry experience in semiconductor wafer fab environment.
- Si or GaAs or GaN experiences desired.

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