Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA. 90505
Telephone: 310-530-7274 Fax: 310-517-8200
Website: www.gcsincorp.com

Job Title: **CAD / Tapeout Engineer**
Job Req: #0589
Department: Foundry Services
Status: Full-Time/Exempt
Shift: Day

Job Description

- Understand tapeout cycle. Work with customer and process team to support tapeout process. Assemble PCMs. Ability to do data management, such as version control. (30%)
- Mask procurement. PO requisition. Schedule, track, and document orders. (30%)
- Create Wafer-Assembly-Drawing (WAD). Capture and document process requirements. (30%)
- Provide DRC services and support customers on DRC feedback.
- Internal /Customer Support: Perform layout per requirements.
- Work with Production Control team for lot start.
- Support process team for layout illustration.

Job Requirements

- Minimum AA degree. BA or BS degree preferred.
- Minimum 3 years of experience in semiconductor or EDA industry.
- Strong knowledge in layout software, such as Cadence Virtuoso, Assura and other CAD software.
- Knowledge of Agilent ADS, AWR MWO circuit simulation and other layout software is a plus.
- PDK working experience desired.
- Knowledge of UNIX or Linux.
- Strong knowledge in Microsoft Windows and Microsoft Office.
- Proficient in typing.
- Ability to learn and perform in a fast-paced environment.
- Excellent interpersonal skills required.
- Must be good in written and verbal communication.