



Global Communication Semiconductors, LLC

Corporate & Foundry

23155 Kashiwa Court, Torrance, CA. 90505

Telephone: 310-530-7274 Fax: 310-517-8200

Website: www.gcsincorp.com

Job Req. #0602
Job Title: Device / Process Integration Engineer
Department: Technology
Status: Full-Time/ Exempt

Job Description

- Responsible for GaN HEMT process development and yield improvement efforts, including 0.5um and 0.25 um processes
- Responsible for device characterization and process qualifications
- Support trouble-shooting and yield improvement in pHEMT, HFET and MESFET production processes.

Job Requirements

- PhD in Electrical Engineering, Physics, Materials Science or equivalent.
- 3-5+ years of industry experience in semiconductor wafer fab environment desirable.