Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA. 90505
Telephone: 310-530-7274 Fax: 310-517-8200
Website: www.gcsincorp.com

Job Title: **Applications Engineer**
Job Req: #0590
Department: Foundry Services
Status: Full-Time/Exempt
Shift: Day

Job Description

- **Device Characterization**: Perform on-wafer dc and RF load/source pulls to characterize and optimize devices RF performance such as: P-1dB, Psat, Gain, OIP3, linearity, efficiency and noise figure, etc.
- **Evaluation Board Design:** design, prototype development and testing of evaluation boards to optimize device performance for gain, output power and noise figure.
- **Design Kit Development**: Prepare, verify and document device models, design rules and cell libraries in Agilent ADS and Microwave Office AWR software.
- **Data sheet and Applications Notes:** generate and update data sheets and application notes for foundry processes.

Job Requirements

- BSEE minimum, MSEE preferred. A minimum of 5 year experience in wireless and semiconductor industry.
- Good knowledge of RF design, impedance matching, hands-on experience in testing and evaluation board proto-type development. Good understanding of GaAs semiconductor process flows and devices, such as HBT, PHEMT, etc.
- Experience in fully calibrating and operating Maury and Focus load pull stations preferred.
- Hands on experience using RF & DC test equipment.
- Working knowledge of circuit simulation software such as Agilent ADS and Microwave Office AWR. Knowledge of layout software such as Cadence.
- Experience in ADS PDK development, including 2011 compatible PDK conversion.
- Knowledge of VBIC, Gummel-Poon, EEHEMT, Angelov and other non-linear models.
- Knowledge of DOE, Process/Design reviews, Failure analysis, CAD Layout, DRC and mask tape out.
- Experience in Windows, Unix and Linux systems.
- Excellent knowledge of all Microsoft Office products.
- Attention to detail required.
- Excellent communication skills orally and in writing.