



Global Communication Semiconductors, LLC

(A wholly owned subsidiary of GCS Holdings, Inc.)

Corporate Office & Foundry

23155 Kashiwa Court, Torrance, CA 90505

Tel. 310-530-7274 Fax. 310-517-8200

Req. No.: Req. No. 440

Posting Date: January 29, 2013

Position: Sr. Process Engineer

Duties:

1. Serve as lot integration engineer to oversee process lots for development as well as contract deliverables:
 - a. mask layout review
 - b. process flow review
 - c. follower preparation
 - d. lot starts
 - e. wafer/lot and test data delivery
2. Internally serve as group leader in laser technical areas (discrete and photonics IC) with emphasis on orchestrating teamwork through close coordination with colleagues in the Opto team as well as Fab Modular Engineering and Production.
3. Externally provide interactions with customers and vendors as necessary

Location: Torrance, CA

Compensation: Based on Experience

Education: M.S./Ph.D. in Electrical Engineering/Physics/Chemistry

Other Requirements: 5 – 10 years experience in III-V compound semiconductor devices, wafer fab processing, device testing and some project management skills.

Contact: Sue Morton, Human Resource Manager
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Sue Morton

Sue Morton, Human Resource Manager

1/29/13

Date