

Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA. 90505
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PROCESS INTEGRATION ENGINEER

- 1. Direct or perform process development and process integration work for HV SiC device, RF GaN HEMT and other new applications.
- 2. Interact with customers and process module engineers at the technical level to define and optimize process flow and devices.
- 3. Analyze electrical data to improve device characteristics.
- 4. Conduct training for operators and technicians.
- 5. Transfer technology from the R&D level to the production level.

Require Master's degree in Optical Engineering, Electrical Engineering, or a related field, plus one year of experience in job offered or in device/process engineering.

Require experience in semiconductor process design (DOE), SEM, dry etch, wet etch, photolithography, process mode effect failure analysis (PFMEA), and customer support.

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