



Global Communication Semiconductors, LLC

Corporate & Foundry
23155 Kashiwa Court, Torrance, CA 90505
Telephone: 310-530-7274 Fax: 310-517-8200
Website: www.gcsincorp.com

Job Title: **FOUNDRY SUPPORT ENGINEER**
Job Req: #0694
Status: Full-Time, Exempt

Duties

- Understand tape out cycle. Work with customers and process team to support tape out process.
- Maintain and develop Process Design Kit (PDK) - Advance Design System (ADS) and Micro Wave Office (MWO) - to support foundry processes.
- Create Wafer-Assembly-Drawing (WAD). Captures and documents process requirement.
- Provide Design Rule Check (DRC) services and support customers on DRC feedback.
- Provide CAD services by performing Integrated Circuit layout per customer's requirements.
- Assemble PCMs (Process Control Monitor).
- Mask Procurement; PO requisition; schedules; tracks and document order.
- Provide internal as well as customer support.

Requirements

BS in Electrical Engineering or related field with two years of experience in semiconductor field or EDA (Electronic Design Automation) environment.

Experience in mixed signal IC design and layout software, such as Cadence Virtuoso, Assura and other CAD software.

Experience in Agilent ADS, circuit layout and simulation and other CAD software.

Experience in RF measurement and knowledge of Unix or Linux system.

Experience in CAD software such as CADENCE Virtuoso, Assura and Diva, DW-2000, K layout and other commercially available CAD layout tools; Agilent ADS and other simulation software; Spread sheet software; Presentation software.

Will accept MS in Electrical Engineering or related field with coursework and projects in Mixed Signal IC Design, CMOS circuit design, Microelectronics and Nano devices, VLSI design, VLSI/Nano Technology and devices, Digital Electronics, as the equivalent.

LOCATION: Torrance, CA

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