

0.5um ED-Mode PHEMT Process



- **GCS has developed a true E-mode high-efficiency power PHEMT process with optional E/D-PHEMT available for integrated switch, amplifier and digital control functions**
- **E-mode PHEMT has extremely low leakage current at zero gate bias**

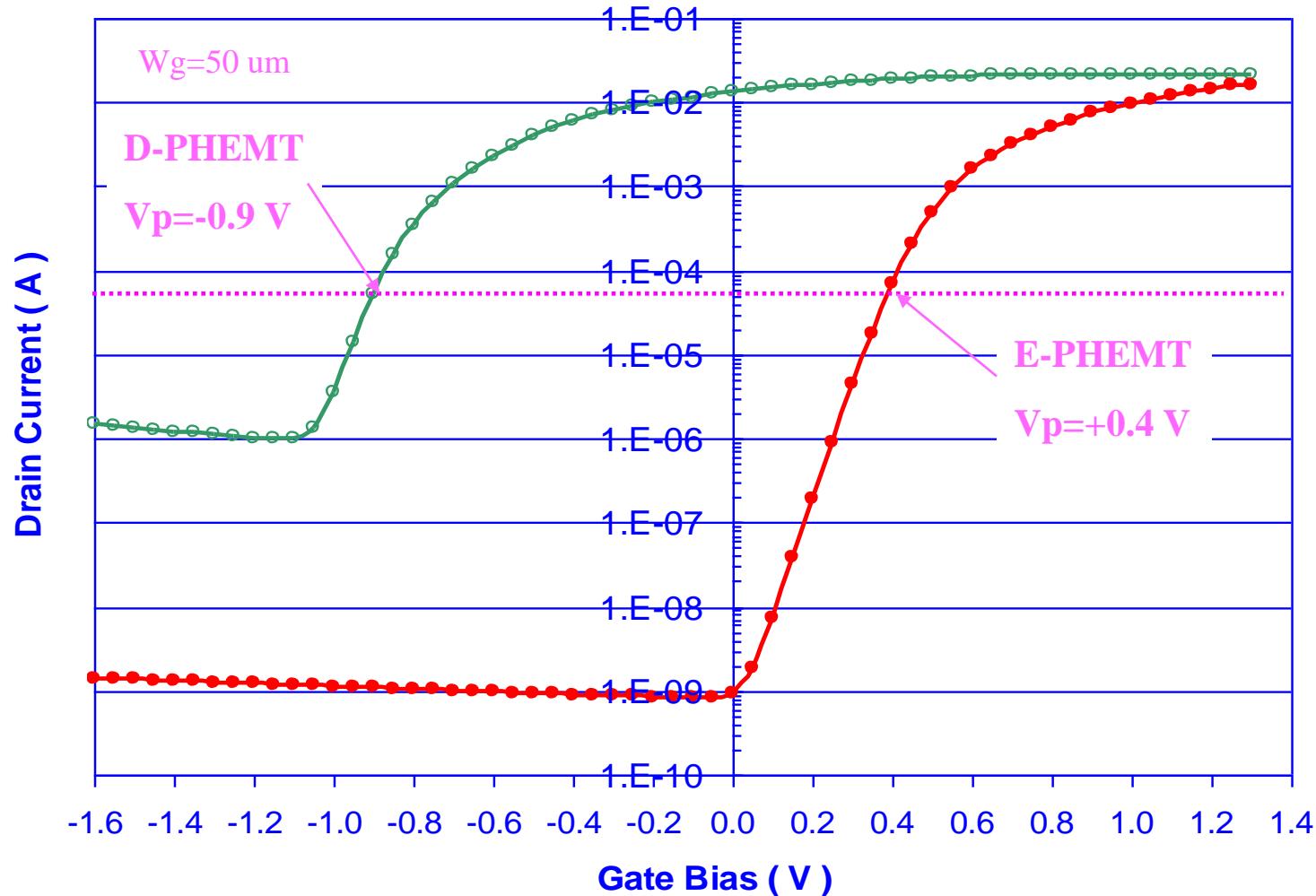
0.5um ED-Mode Characteristics



	D-PHEMT			E-PHEMT		
Parameters	min	max	typical	min	max	typical
V _p (V)	-1.1	-0.7	-0.9	0.25	0.55	0.4
I _{dss} (mA/mm)	170	330	250	0.000005	0.0001	0.00002
G _m (mS/mm)	280	420	350	400	600	500
I _{max} (mA/mm)	380	520	450	180	320	250
V _{gd} (V)	9	16	12	18	35	28
R _{on} (Ohm.mm)	1.5	2.5	2	1.5	2.5	2
F _t (GHz)	28	38	33	21	35	28
F _{max} (GHz)	55	35	45	55	125	90

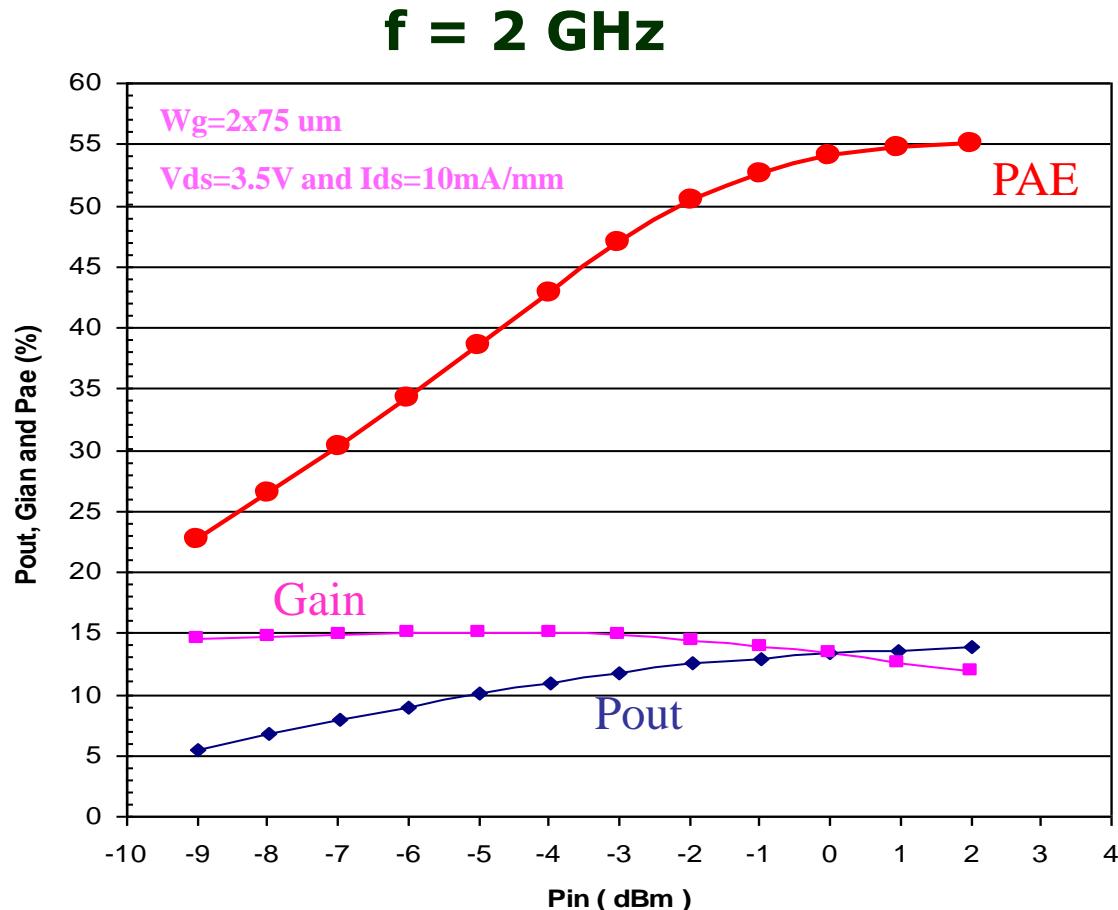
V_p @ I_{ds}=1mA/mm and V_{gd} @ I_{gs}=1mA/mm, R_{on} @ (V_{gs}=0V D-PHEMT) and (V_{gs}=1V E-PHEMT)

0.5um ED-Mode PHEMT Pinch-off Characteristics



E-PHEMT : Idss=20 nA/mm

Power Performance of E-Mode PHEMT



P_{1dB}=135 mW/mm and P_{sat}=165 mW/mm